

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Atty. Dkt. No. 5298-02501

PM98019C


Inventor(s):

Anantha R. Sethuraman

Christopher A. Seams

Title: PLANARIZED SEMICONDUCTOR
INTERCONNECT TOPOGRAPHY
AND METHOD FOR POLISHING
A METAL LAYER TO FORM
INTERCONNECT

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<p>CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. § 1.10</p> <p>"Express Mail" mailing label no. <u>EL726370210US</u> Date of Deposit: <u>February 7, 2001</u></p> <p>I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to: Assistant, Commissioner for Patents, Box Patent Application, Washington, DC 20231.</p> <p> Derrick Brown</p>
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INFORMATION DISCLOSURE STATEMENT


Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant requests consideration of the references listed on the attached Form PTO-1449. The references listed on the Form PTO-1449 were previously cited in application serial no. 09/143,723 to which the captioned application is seeking priority under 35 U.S.C. § 120; therefore, a copy of each reference is not enclosed.

No fee is believed to be required. If a fee is required, the Commissioner is authorized to charge Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5298-02501.

Respectfully submitted,



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